

DT-1V*2D1F/2D1F-X

PINOUT DIAGRAM

STATUS INDICATORS
1. TX CARRIER (GREEN)/ERROR (RED)

STATUS INDICATORS

1. POWER (GREEN)
2. NA

VIDEO INPUT (CH.1)

STATUS INDICATORS

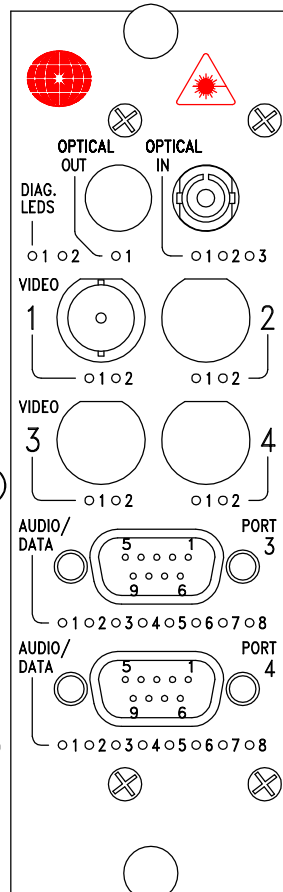
1. SYNC PRESENT (GREEN)
2. VIDEO PRESENT (GREEN)OVERLOAD (RED)

DATA RS232 IN/OUT (PORT 3)
(DB-9 FEMALE)

1. (CH.4) NA
2. (CH.1) OUTPUT (TD)
3. (CH.1) INPUT (RD)
4. (CH.2) INPUT (DTR)
5. GND
6. (CH.2) OUTPUT (DSR)
7. (CH.3) NA
8. (CH.3) NA
9. (CH.4) NA

ATA RS422 IN/OUT (PORT 4)
(DB-9 FEMALE)

1. (CH.1) INPUT +
2. (CH.1) INPUT -
3. (CH.2) NA
4. (CH.2) NA
5. GND
6. (CH.1) OUTPUT +
7. (CH.1) OUTPUT -
8. (CH.2) NA
9. (CH.2) NA



OPTICAL PORT

STATUS INDICATORS

1. NA
2. RX OPTICAL SIGNAL (GREEN)/ABSENT (RED)
3. RX CARRIER (GREEN)/ERROR (RED)

STATUS INDICATORS (PORT 3)

1. (CH.1) DATA INPUT
2. (CH.1) DATA OUTPUT
3. (CH.2) DATA INPUT
4. (CH.2) DATA OUTPUT
5. NA
6. NA
7. NA
8. NA

STATUS INDICATORS (PORT 4)

1. (CH.1) DATA INPUT
2. NA
3. NA
4. NA
5. NA
6. (CH.1) DATA OUTPUT
7. NA
8. NA

*U-10MHz, 10 BIT VIDEO

*V-8 BIT VIDEO

*W-10 BIT VIDEO

MERIDIAN TECHNOLOGIES INC.
FILE NAME: DTDR-1V2D1F-2D1F.DWG
02/01/2007 V.0

DR-1V*1D1F/1D1F-X

PINOUT DIAGRAM

STATUS INDICATORS

1. TX CARRIER (GREEN)/ERROR (RED)

STATUS INDICATORS

1. POWER (GREEN)
2. NA

VIDEO OUTPUT (CH.1)

STATUS INDICATORS

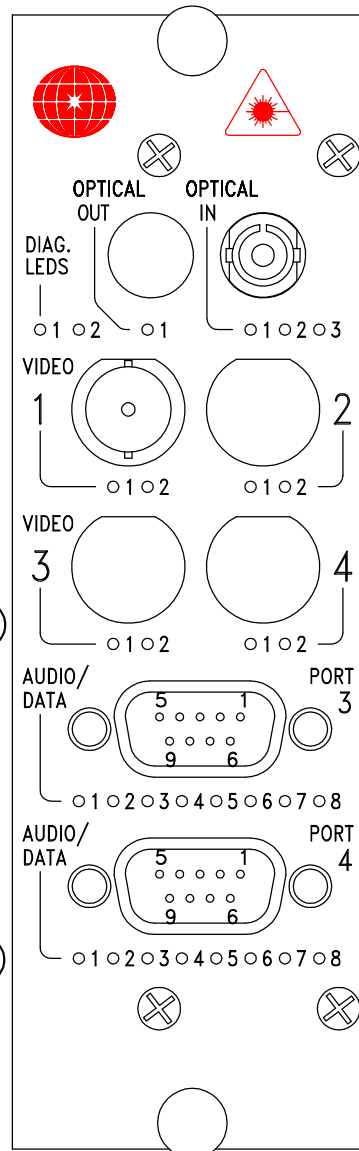
1. SYNC PRESENT (GREEN)
2. VIDEO PRESENT (GREEN)/OVERLOAD (RED)

DATA RS232 IN/OUT (PORT 3) (DB-9 FEMALE)

1. (CH.4) NA
2. (CH.1) OUTPUT (TD)
3. (CH.1) INPUT (RD)
4. (CH.2) INPUT (DTR)
5. GND
6. (CH.2) OUTPUT (DSR)
7. (CH.3) NA
8. (CH.3) NA
9. (CH.4) NA

DATA RS422 IN/OUT (PORT 4) (DB-9 FEMALE)

1. (CH.1) OUTPUT +
2. (CH.1) OUTPUT -
3. (CH.2) NA
4. (CH.2) NA
5. GND
6. (CH.1) INPUT +
7. (CH.1) INPUT -
8. (CH.2) NA
9. (CH.2) NA



OPTICAL PORT

STATUS INDICATORS

1. NA
2. RX OPTICAL SIGNAL (GREEN)/ABSENT (RED)
3. RX CARRIER (GREEN)/ERROR (RED)

STATUS INDICATORS (PORT 3)

1. (CH.1) DATA INPUT
2. (CH.1) DATA OUTPUT
3. (CH.2) DATA INPUT
4. (CH.2) DATA OUTPUT
5. NA
6. NA
7. NA
8. NA

STATUS INDICATORS (PORT 4)

1. NA
2. (CH.1) DATA OUTPUT
3. NA
4. NA
5. (CH.1) DATA INPUT
6. NA
7. NA
8. NA

*U-10MHz, 10 BIT VIDEO

*V-8 BIT VIDEO

*W-10 BIT VIDEO